GAU, Faculty of Engineering

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Cour	se Unit Title	Electronic Circuits I					
Cour	se Unit Code	EEN301					
Туре	of Course Unit	Compulsory, Electrical and Electronics Engineering students					
Leve	l of Course Unit	3rd Year BSc					
Natio	onal Credits	4					
Num	ber of ECTS Credits Allocated	7 ECTS					
Theo	oretical (hour/week)	3					
Pract	tice (hour/week)	-					
Labo	ratory (hour/week)	2					
Year	of Study	3					
Semo	ester when the course unit is delivered	5					
Mod	e of Delivery	Face to Face, Laboratory Experiments,					
lang	uage of Instruction	English					
Prere	equisities and co-requisities	ENG202 Physical Electronics					
		Basic background of Fundamentals	of Electr	rical			
Reco	ommended Optional Programme Components	Engineering					
Obje	ctives of the Course:						
\blacktriangleright	Teaching DC biasing and DC analysis of transisto	r amplifiers					
\blacktriangleright	Teaching AC (small signal) analysis of basic transistor amplifier stages						
	Teaching the operational amplifier its application	ns					
Lear	ning Outcomes						
Whe	hen this course has been completed the student should be able to Ass						
1	conduct DC analysis of basic transistor amplifiers						
2	bias transistors in proper operating region						
3	conduct AC analysis of basic transistor amplifier stages						
4	design single- and multi-stage amplifiers for low frequencies						
5	analyze and design operational-amplifier-based circuits						
6	test basic transistor amplifiers and operational amplifier circuits via measurements						
Accessment Methods: 1. Written Evam 2. Accignment 2. Project/Deport 4. Procentation 5.5							
		ment, 3. Hoject/keport, 4. Hesentation, 5 L					
Cour	se's Contribution to Program						
			C	L			
1	Ability to understand and apply knowledge of mathematics, science, and engineering						
2	Ability to design and conduct experiments as well as to analyze and interpret data						
2	Ability to work in multidisciplinary teams while exhibiting professional responsibility and						
5	ethical conduct						
4	Ability to apply systems thinking in problem solving and system design						
5	Knowledge of contemporary issues while continuing to engage in lifelong learning						
6	Ability to use the techniques, skills and modern engineering tools necessary for engineering practice						
	Ability to express their ideas and findings, in written and oral form						
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7 8	Ability to express their ideas and findings, in we Ability to design and integrate systems, compone realistic constraints	ritten and oral form tents or processes to meet desired needs with	in 5	5			
7 8 9	Ability to express their ideas and findings, in wr Ability to design and integrate systems, compon realistic constraints Ability to approach engineering problems and er structured, ethically responsible and professional	ritten and oral form ients or processes to meet desired needs with ffects of their possible solutions within a wel il manner	in 5	5			
7 8 9 10	Ability to express their ideas and findings, in wr Ability to design and integrate systems, compon- realistic constraints Ability to approach engineering problems and e structured, ethically responsible and professiona Strong foundation on the fundamentals of Electr Theory, Signals, Systems, Control and Commun- practice in the field	ritten and oral form nents or processes to meet desired needs with ffects of their possible solutions within a wel al manner rical and Electronics Engineering such as Cir nications, which are necessary for successful	in 5 1 3 cuit 5	3			

Electronics Engineering CL: Contribution Level (1: Very Low, 2: Low, 3: Moderate, 4: High, 5:Very High)

Course Contents									
Week									
1	Overview on physical structure, behavior, electrical model and operation regions of bipolar- iunction transistor (BIT)								
2	Characteristics of BJT. Conceptual usage of transistors in analog and digital applications.								
3	Biasing and DC analysis of BJT								
4	Amplification and the gain concept. Amplifier models.								
5	Small signal equivalent and terminal resistances of BJT. AC analysis of BIT.								
6	Gain and input/output resistances of basic BJT amplifier stages.								
7	Analysis of cascade (direct/capacitively-coupled) amplifiers.								
8	Midtern Exam Midter								
9	Differential amplifier, common mode rejection ratio.								
10	Physical structure, behavior, electrical model, operation regions and characteristics of metal- oxide-semiconductor field-effect transistor (MOSEET)								
11	Biasing and DC analysis of MOSFET circuits. Small signal equivalent and terminal resistances of MOSFET.								
12	AC analysis, gain and inp	out/output re	sistances of basic MOSI	ET amplifier sta	iges.		Quiz #2		
13	Operational amplifier (C)pAmp); ideal	and practical behavior.						
14	Linear and non-linear ap	plications of	OpAmp.						
15							Final		
Recomm	nended Sources								
Textbook: R. Boylestad & L. Nashelsky, "Electronic Devices and Circuit Theory", 10th edition, Prentice Hall, 2008. Supplementary Material(s): A. Sedra & K.C. Smith, "Microelectronic Circuits", 6th edition, Oxford University Press, 2010.									
Assessment									
Attenda	nce	5%							
Assignments		10%							
Laboratory		10%							
Midterm Exam		20%	Written						
Quizzes		20%							
Final Exam		35%	Written						
Total		100%							
ECTS All	ocated Based on the St	udent Worl	kload						
Activities				Number	Duration (hour)	Wo	Total Workload(hour)		
Hours p	er week (Theoretical)	15	3		45				
Hours per week (Laboratory)				14	2		28		
Pre-Lab	work preparation before	5	3		15				
Present	ing of observations and	5	6		30				
Prepara	tion of the homeworks	5	5		25				
Quizzes		2	11		22				
Supervis	sion	1	17	17					
Final Exa	am	1	22		22				
Total Workload						204			

6.7 7

Total Workload/30 (h)

ECTS Credit of the Course